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15617 7590 04/19/2004

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July 12, 2004
Date

Kevin L. Daffer

APPLICATION NO.	FILING DATE	FIRST NAMES INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/010,833	12/04/2001	Krishnaswamy Ramkumar	5298-06500	7743

TITLE OF INVENTION: METHOD OF FORMING SEMICONDUCTOR STRUCTURES WITH REDUCED STEP HEIGHTS

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1,330.00	\$0.00	\$1,330.00	07/19/2004
EXAMINER	ART UNIT	CLASS-SUBCLASS			
Deo, Duy Vu Nguyen	1765	438-424.000			

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1 Kevin L. Daffer

2 Mollie F. Letteng

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(A) NAME OF ASSIGNEE: Cypress Semiconductor Corp.

(D) RESIDENCE (CITY & STATE OR COUNTRY): San Jose, CA

Please check the appropriate assignee category indicated below (will not be printed on the patent): ☐ individual ☒ corporation or other private group entity ☐ government

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☒ Issue Fee

☐ Publication Fee

☒ Advance Order - # of Copies 4

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Kevin L. Daffer
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(Date)

7/12/04

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